1. Planning and development

Academic calender

Fig : Main page of academic calender (select brach and semester)

Fig : Select month and year

Vidya Prasarak Mandal's
Maharshi Parshuram College of
Engineering, Velneshwar

Elaborated Timetable (Electronics & Telecomm. Engineering)

August 2018

SUNDAY	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
			1	2	3	4
5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28	29	30	31	

Fig : Select date of the month

Logo.jpg | Vidya Prasarak Mandal's Maharshi Parshuram College of Engineering, Velneshwar

Elaborated Timetable (Electronics & Telecommunication Engineering, Semester- V)

Date:- Monday, 27 August 2018

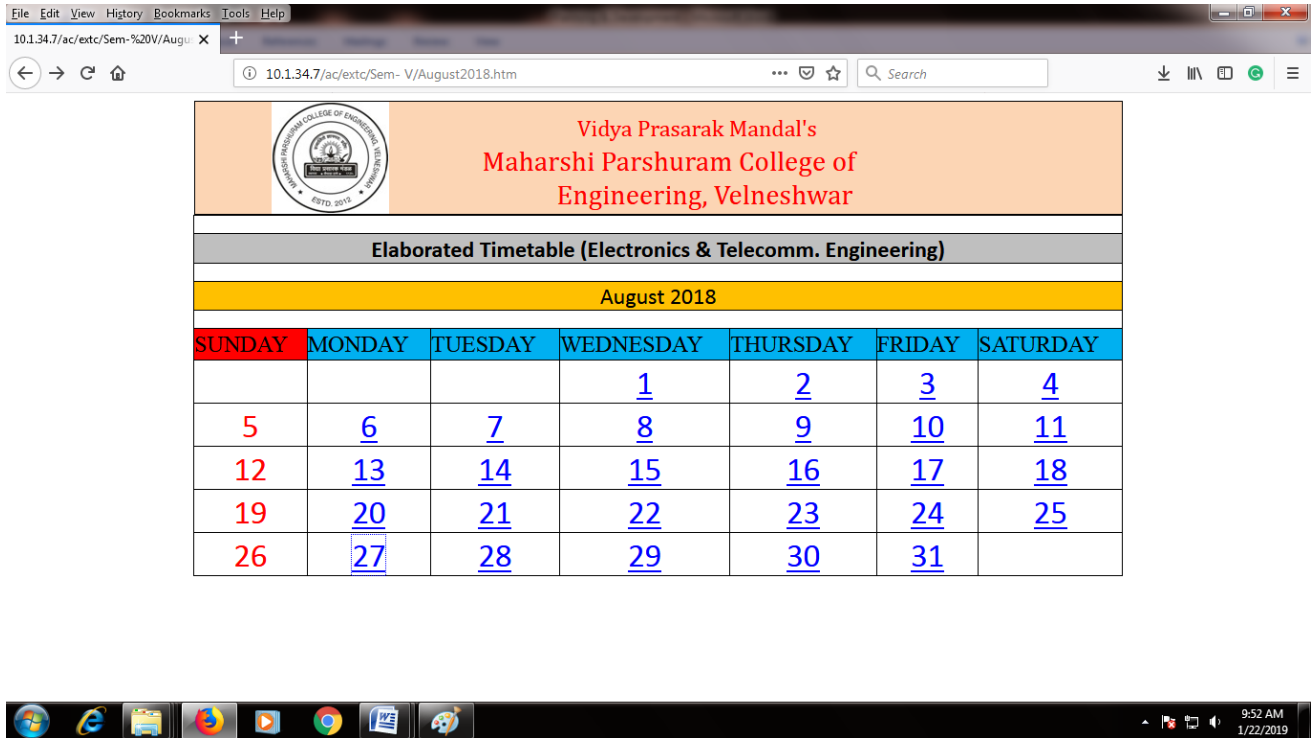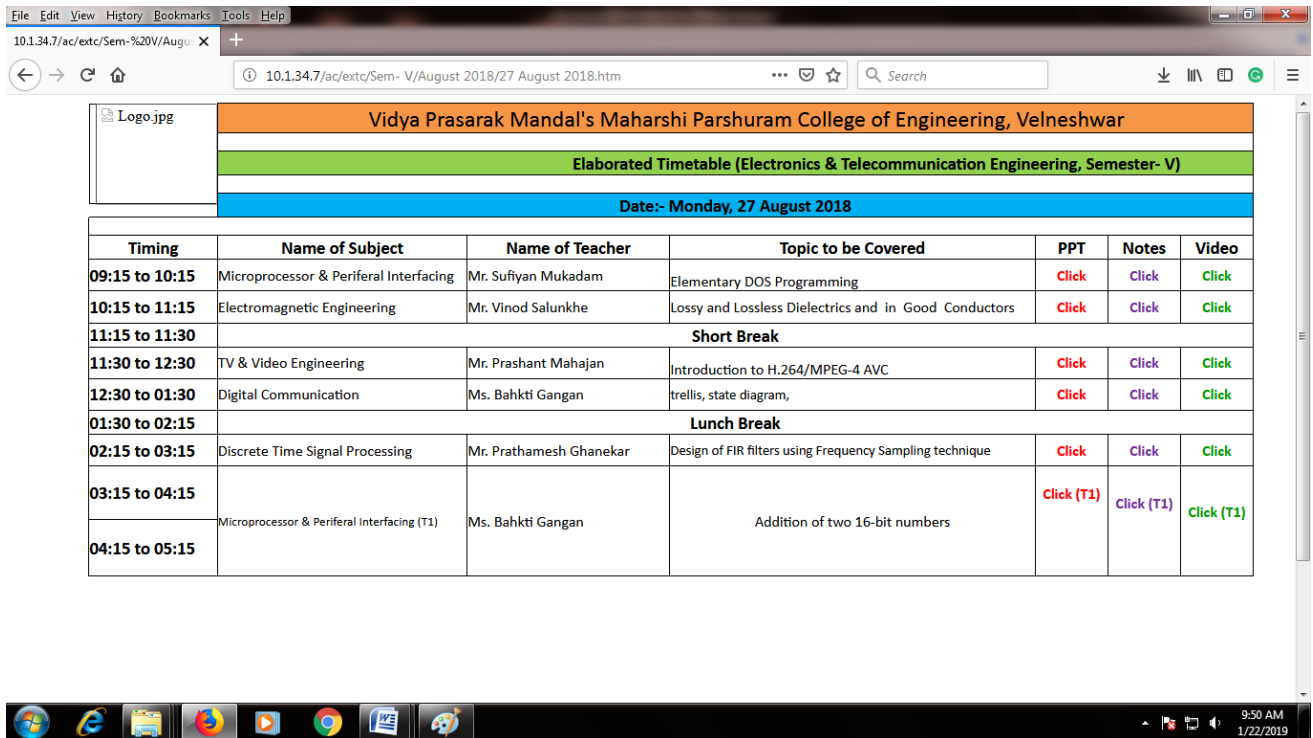| Timing	Name of Subject	Name of Teacher	Topic to be Covered	PPT	Notes	Video
09:15 to 10:15	Microprocessor & Periferal Interfacing	Mr. Sufiyan Mukadam	Elementary DOS Programming	Click	Click	Click
10:15 to 11:15	Electromagnetic Engineering	Mr. Vinod Salunkhe	Lossy and Lossless Dielectrics and in Good Conductors	Click	Click	Click
11:15 to 11:30			Short Break			
11:30 to 12:30	TV & Video Engineering	Mr. Prashant Mahajan	Introduction to H.264/MPEG-4 AVC	Click	Click	Click
12:30 to 01:30	Digital Communication	Ms. Bahkti Gangan	trellis, state diagram,	Click	Click	Click
01:30 to 02:15			Lunch Break			
02:15 to 03:15	Discrete Time Signal Processing	Mr. Prathamesh Ghanekar	Design of FIR filters using Frequency Sampling technique	Click	Click	Click
03:15 to 04:15 / 04:15 to 05:15	Microprocessor & Periferal Interfacing (T1)	Ms. Bahkti Gangan	Addition of two 16-bit numbers	Click (T1)	Click (T1)	Click (T1)

Fig : Topic of teaching and materials for that will be available for students
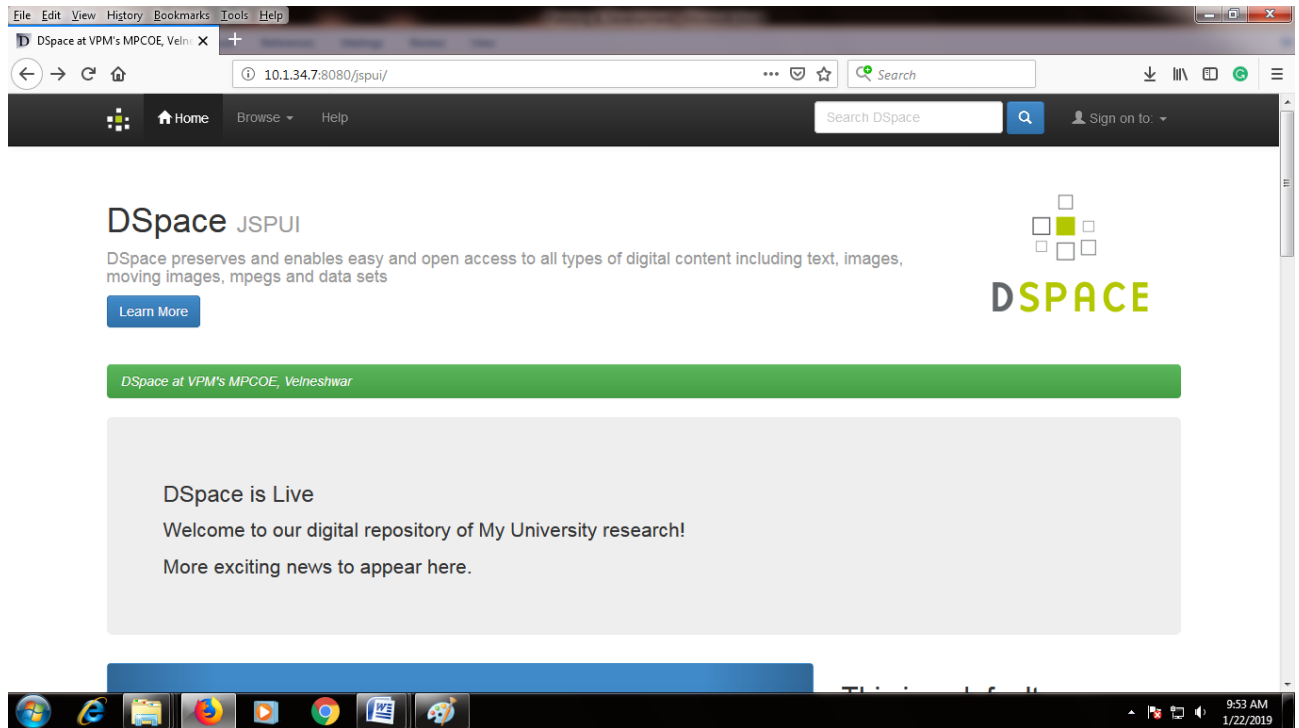
D-space

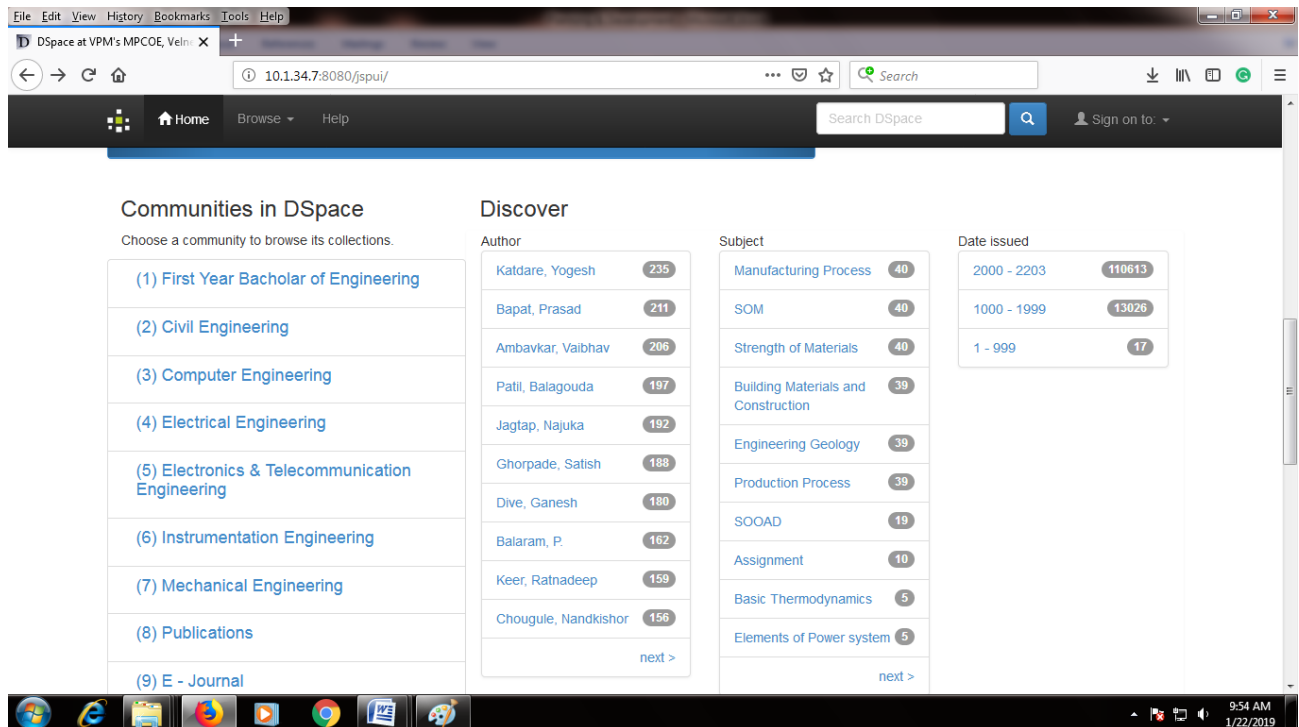**Fig : D-space main window**
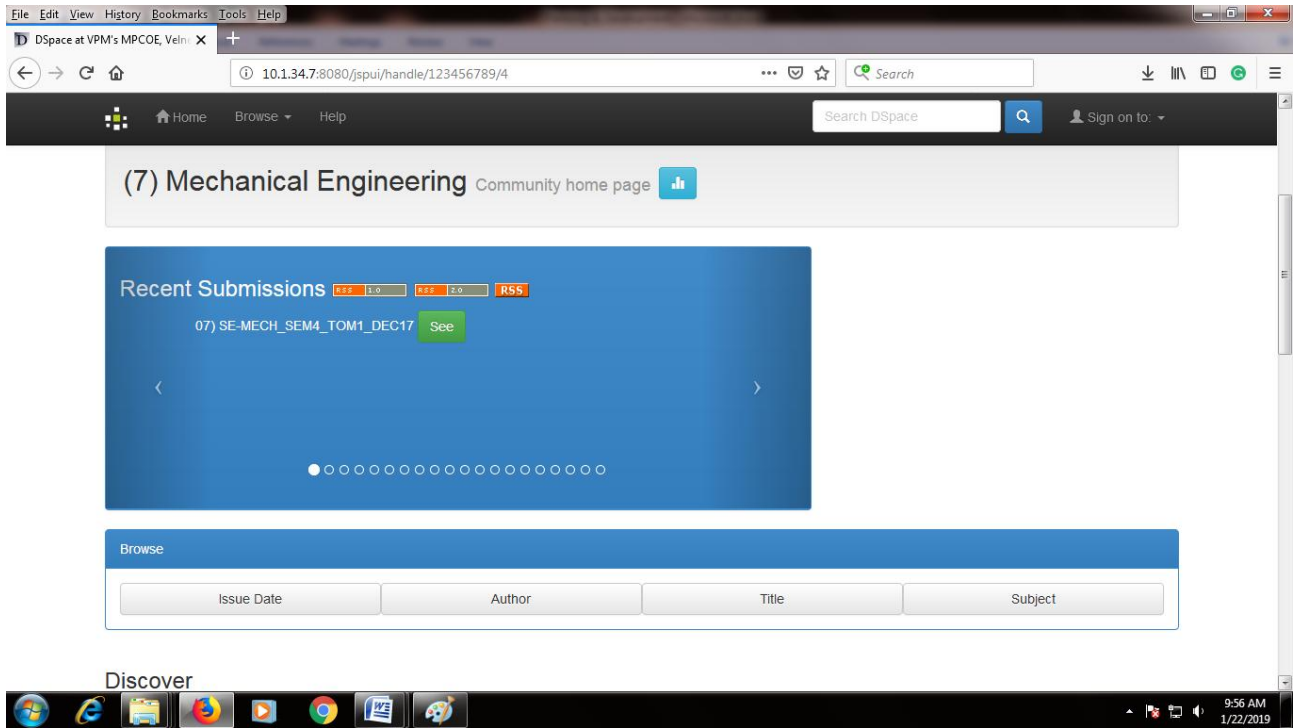

Fig : Communities in D-space

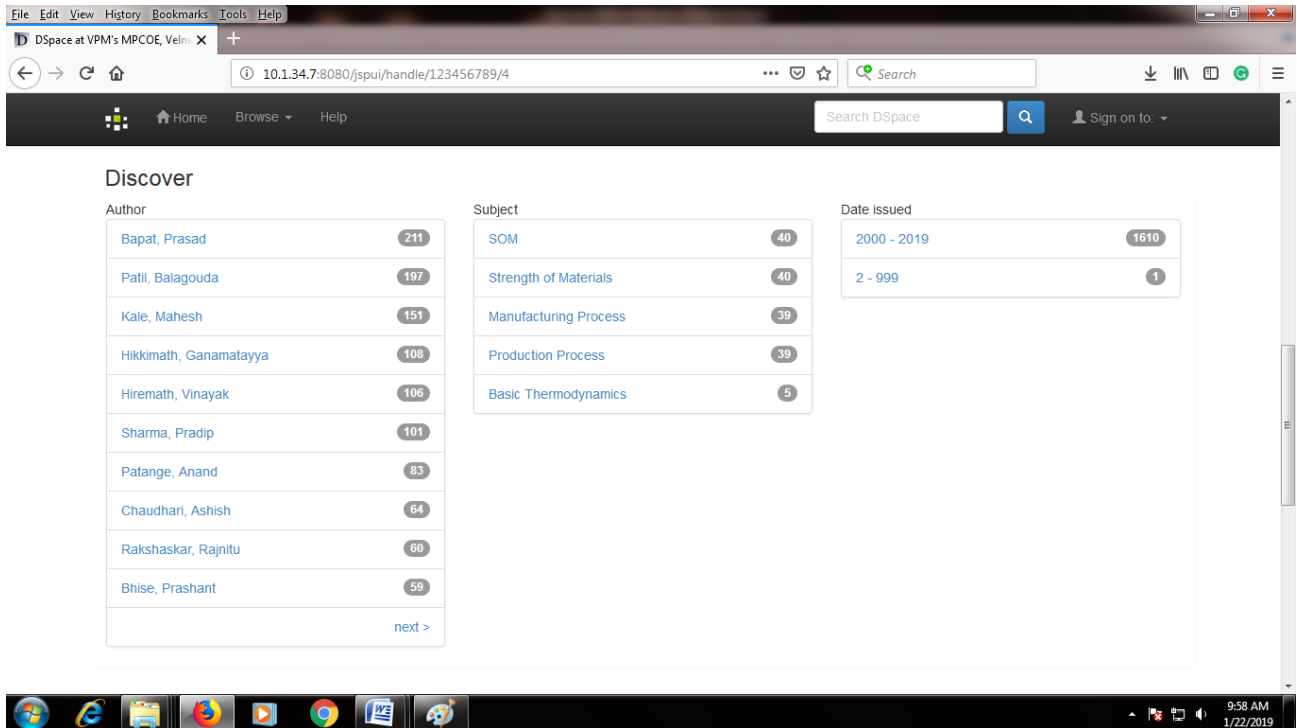**Fig : Mechanical EngineeringCommunity selected in D-space**
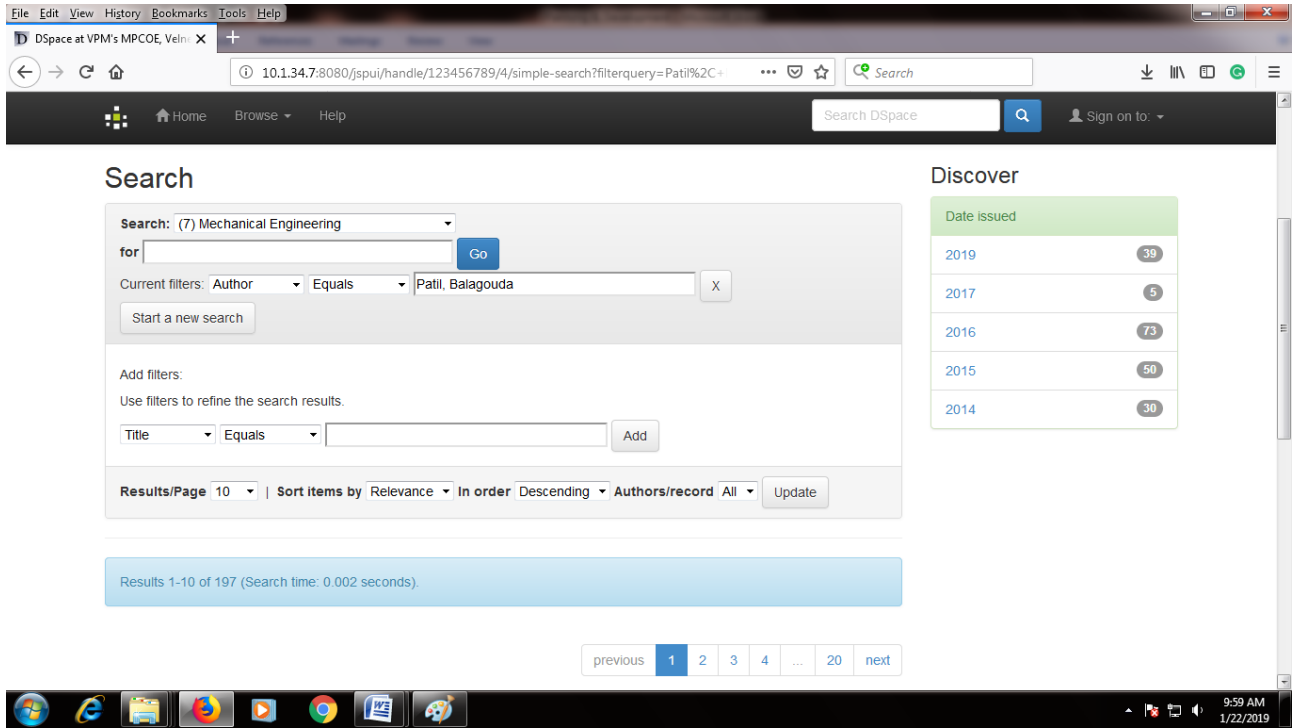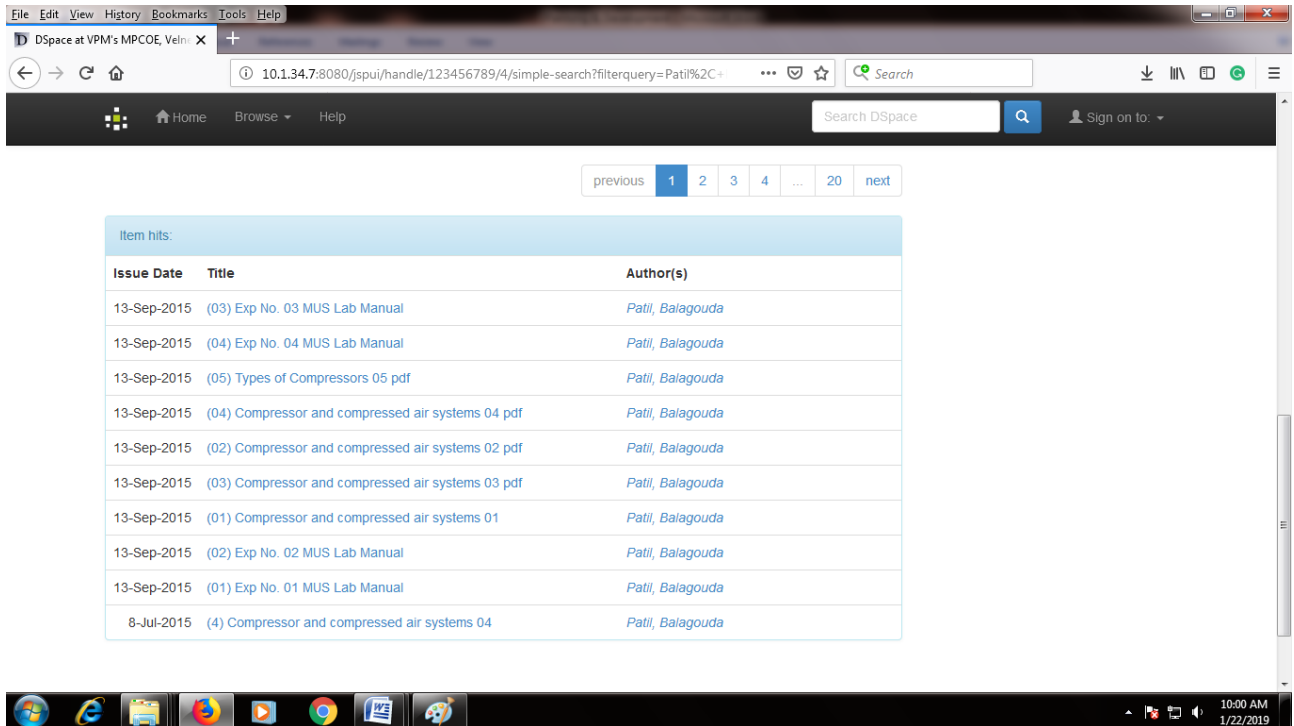

Fig : Authors are visible

Fig : Selecting author

Fig : Materials uploaded by selected author